

FIG. 1

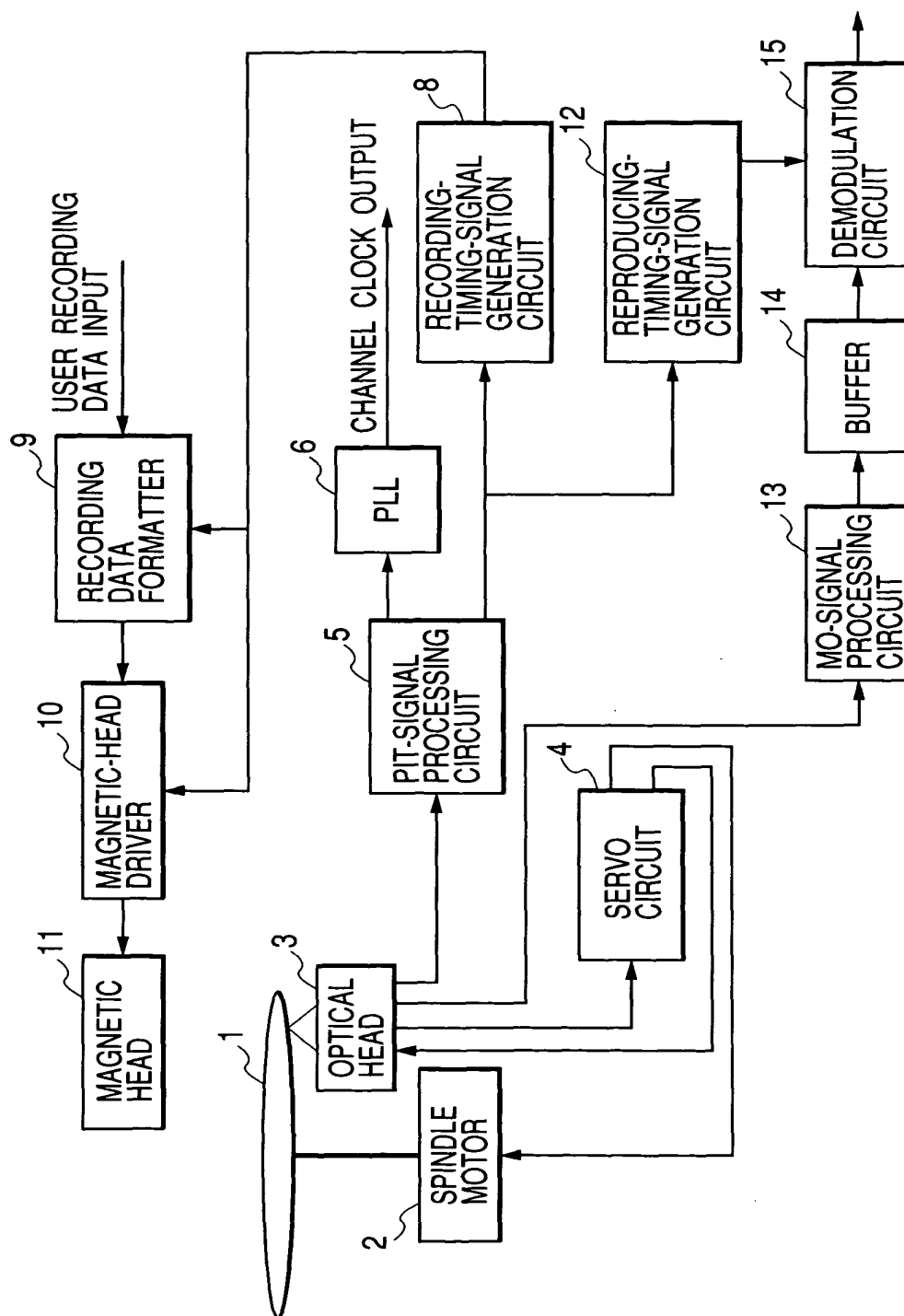


FIG. 2

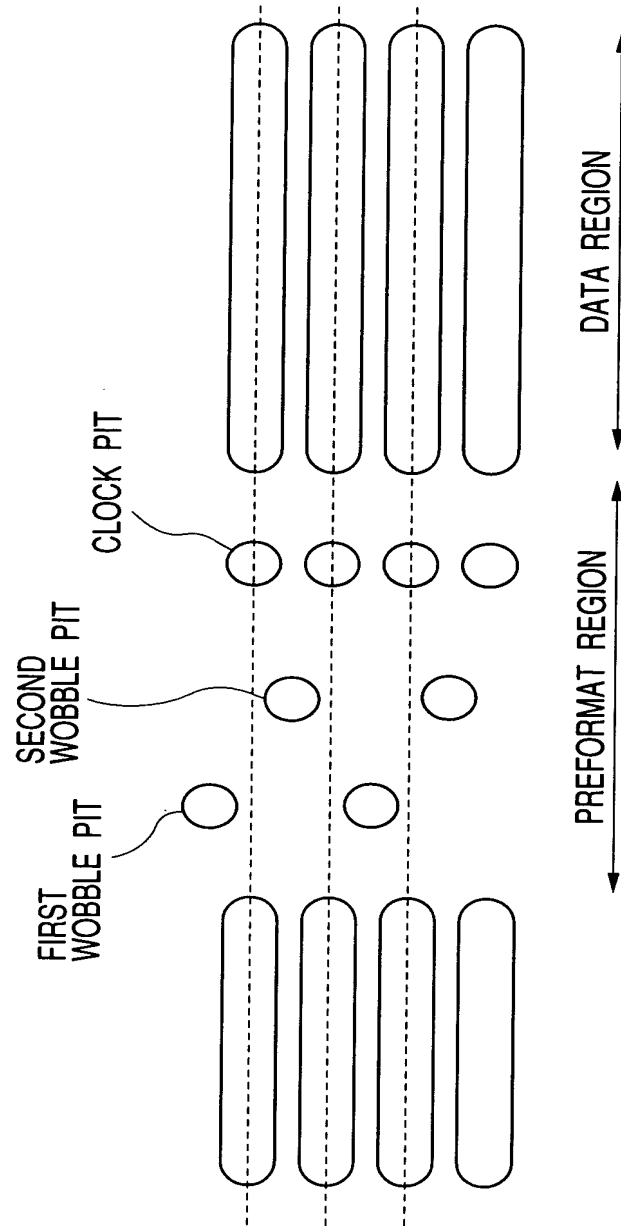


FIG. 3A

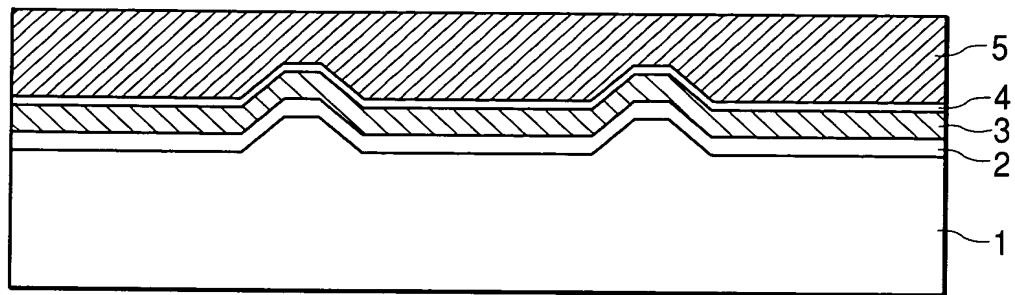


FIG. 3B

UV RESIN
SiN
MEMORY LAYER
SWITCHING LAYER
CONTROL LAYER
DISPLACEMENT LAYER
SiN
SUBSTRATE

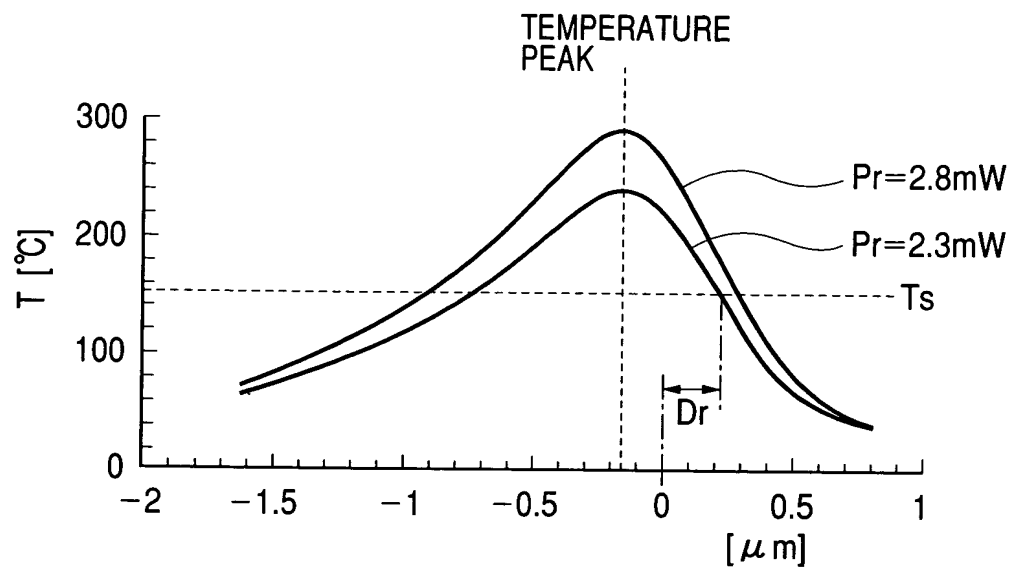
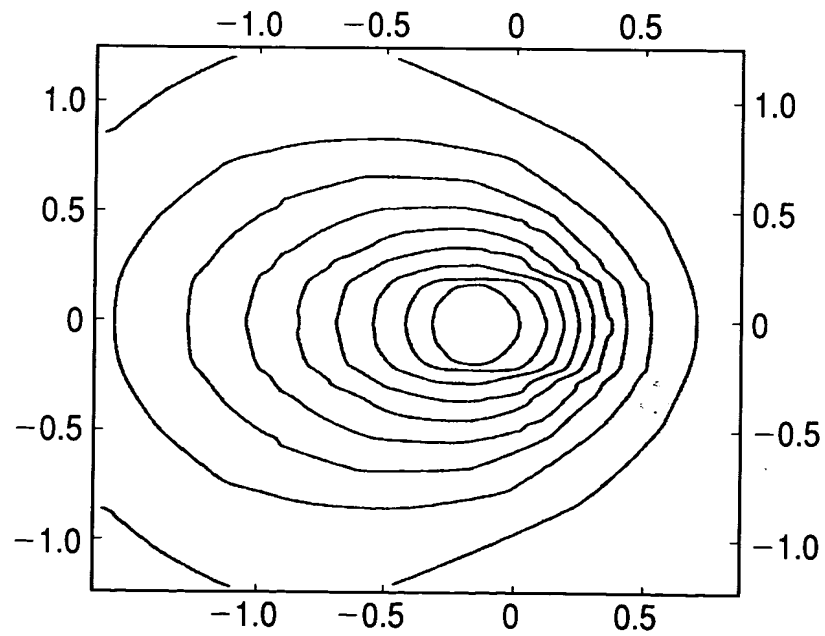
FIG. 4A**FIG. 4B**

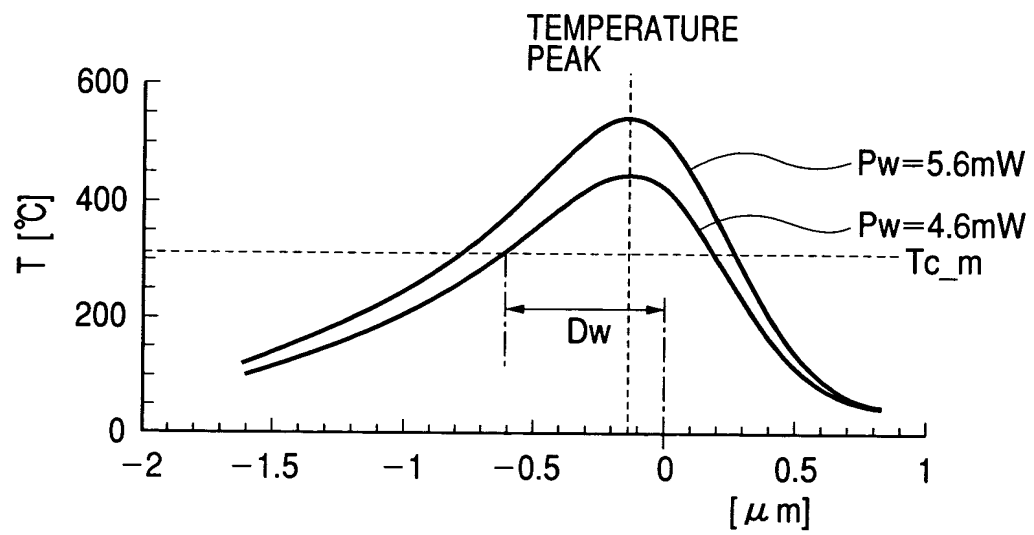
FIG. 5

FIG. 6

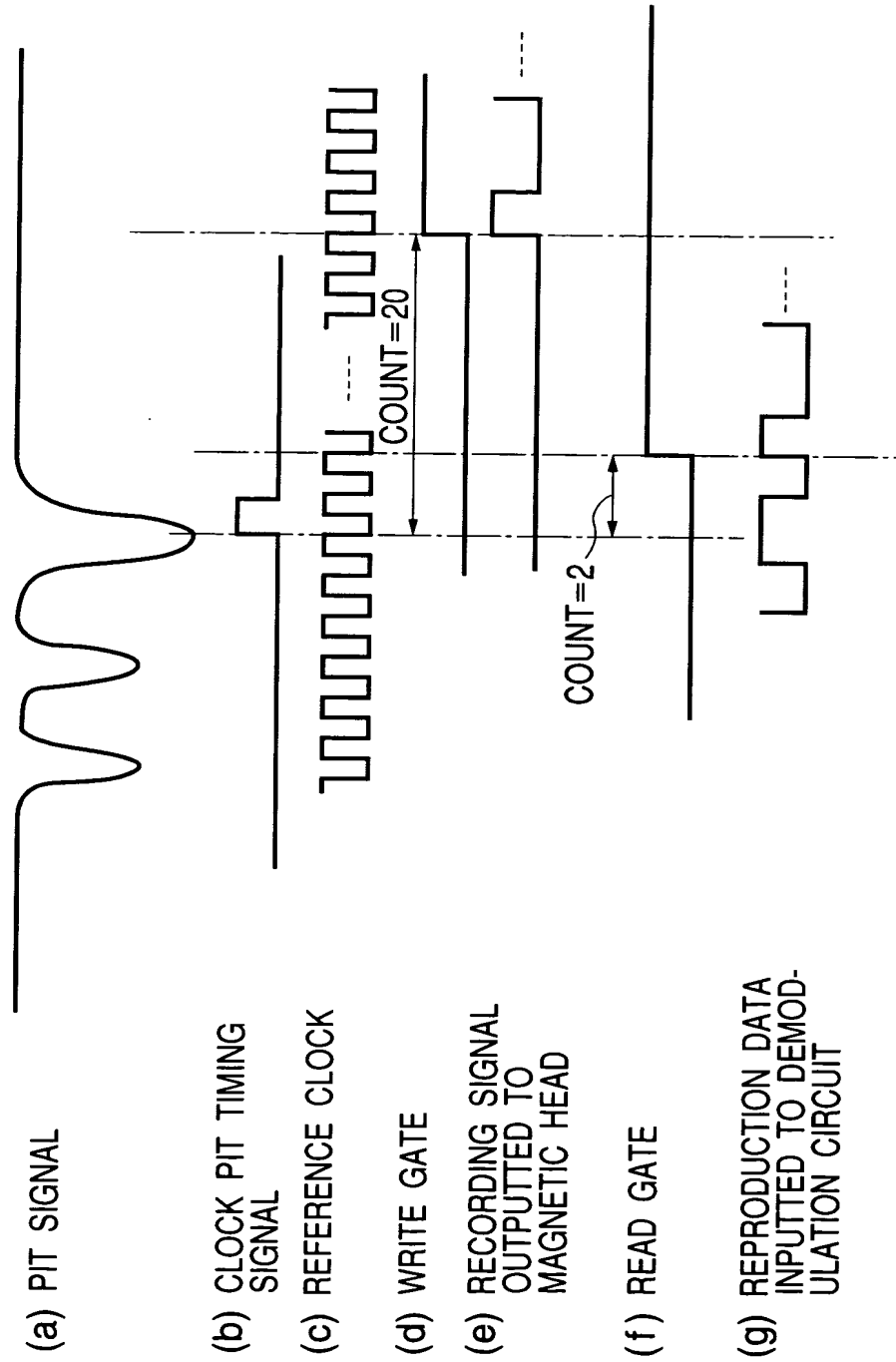


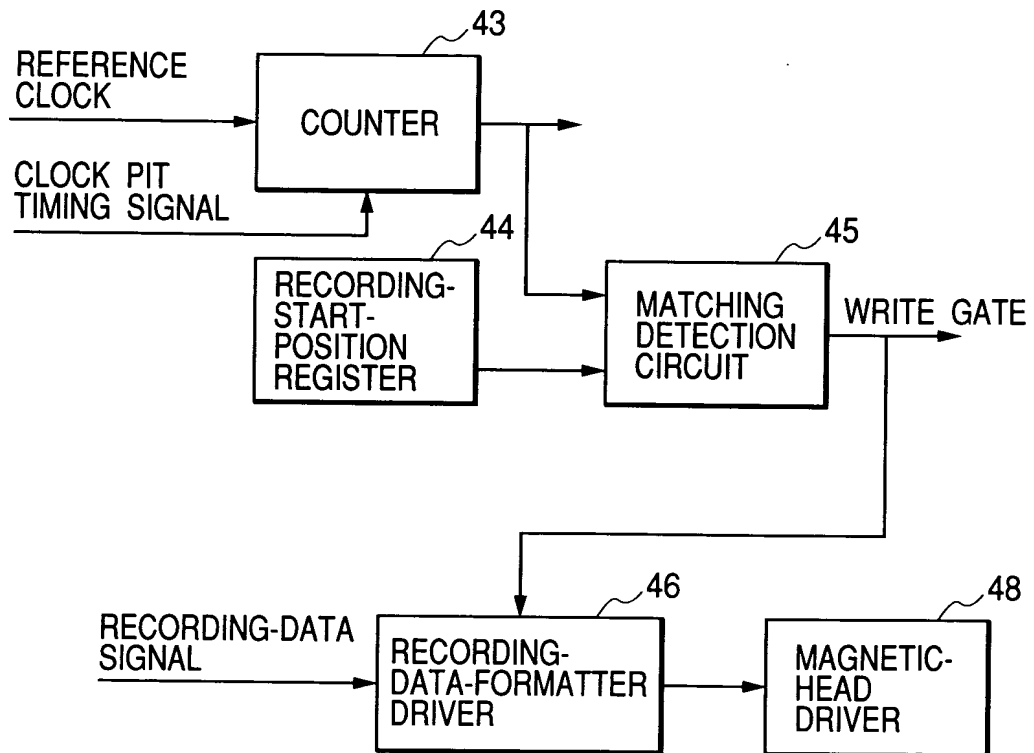
FIG. 7

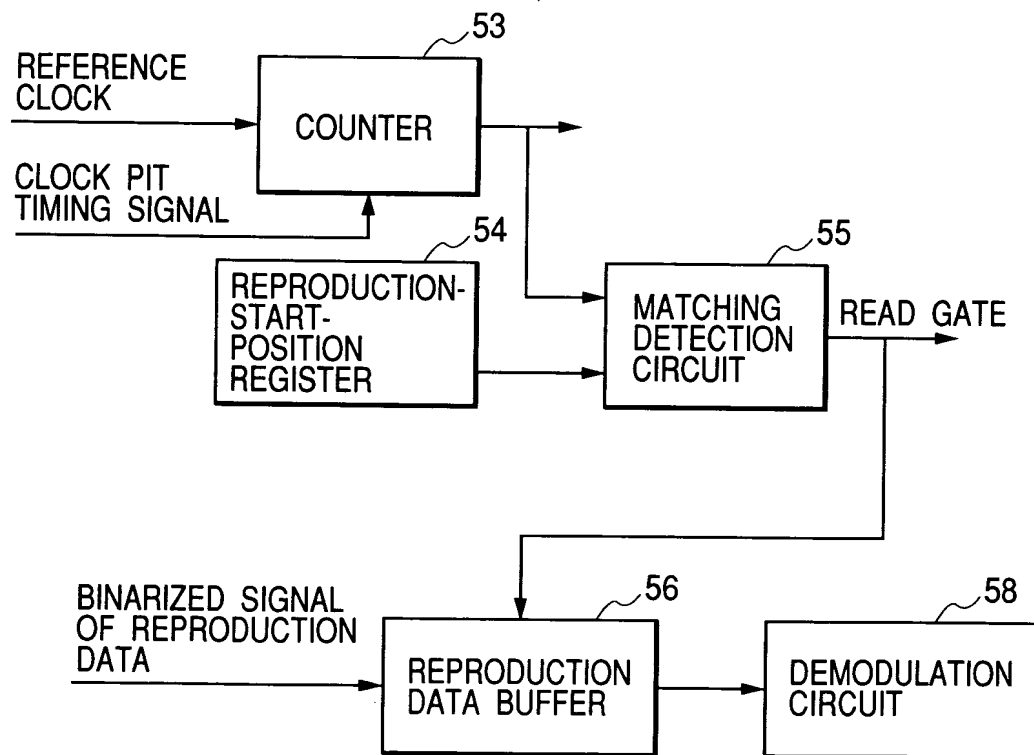
FIG. 8

FIG. 9

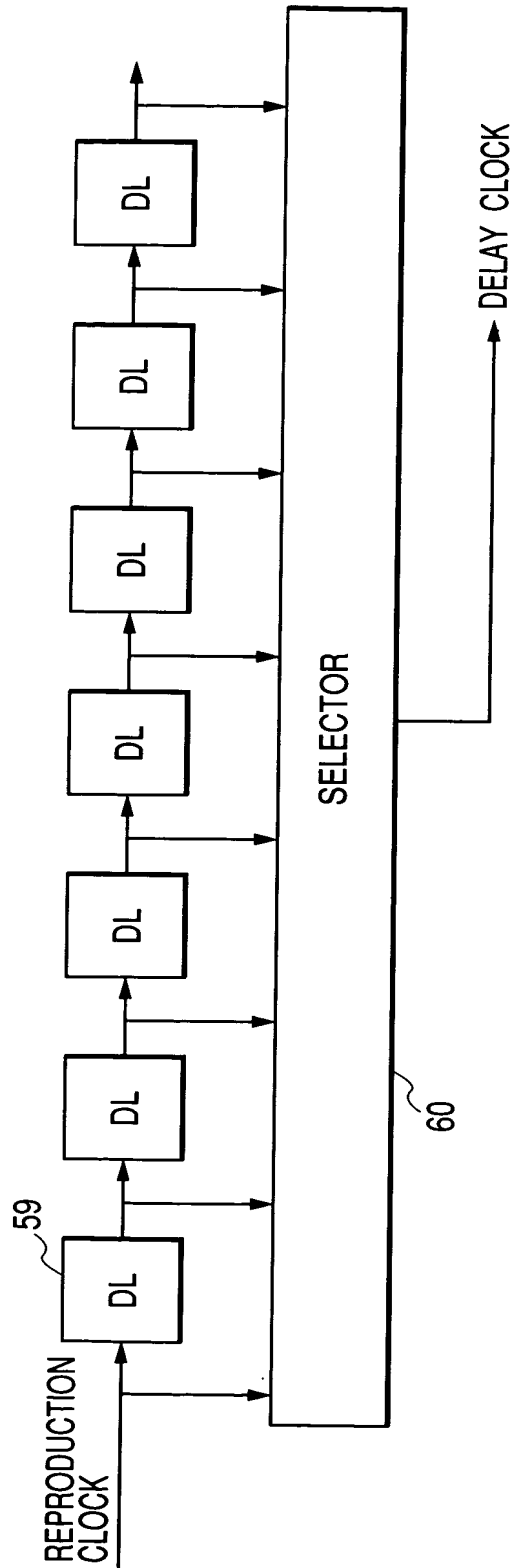


FIG. 10
PRIOR ART

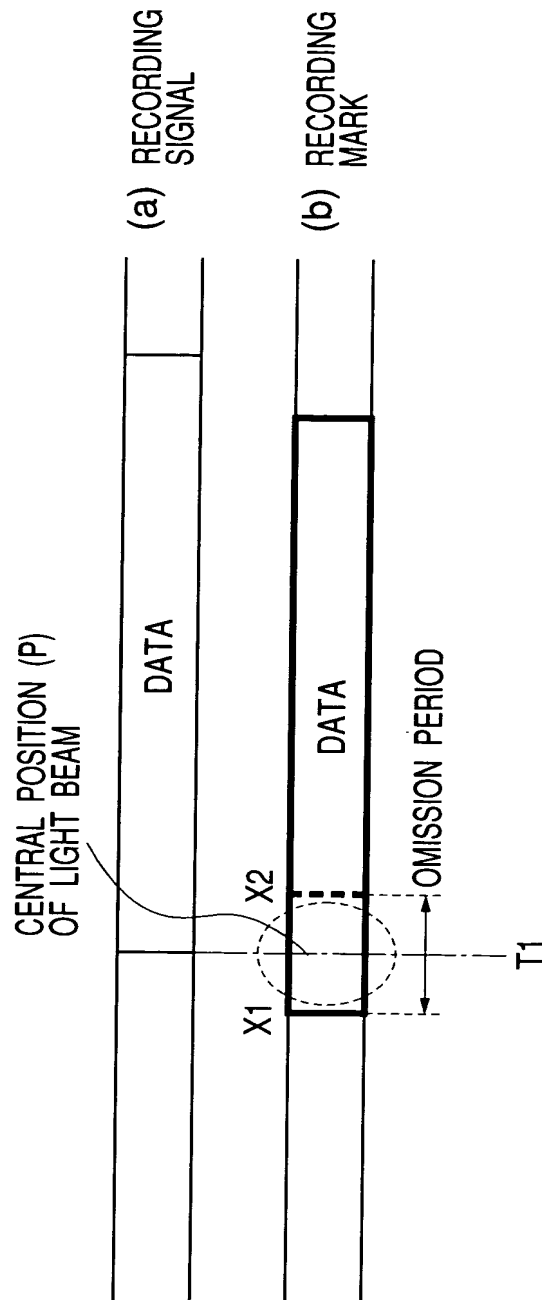


FIG. 11A
PRIOR ART

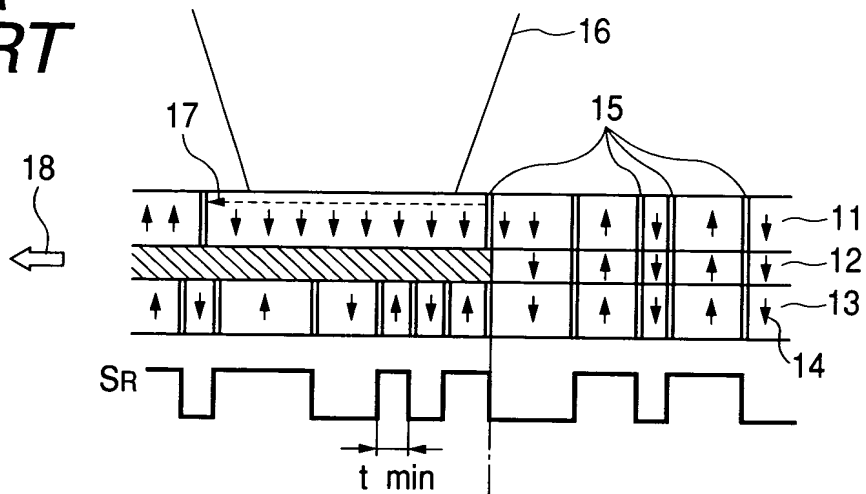


FIG. 11B
PRIOR ART

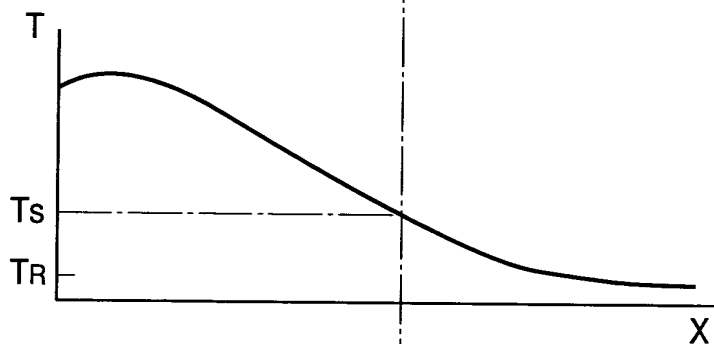


FIG. 11C
PRIOR ART

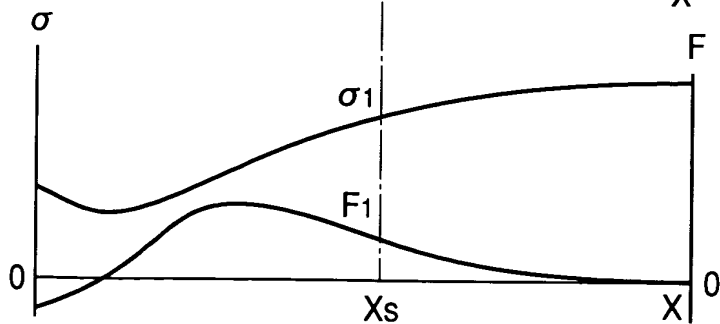


FIG. 13

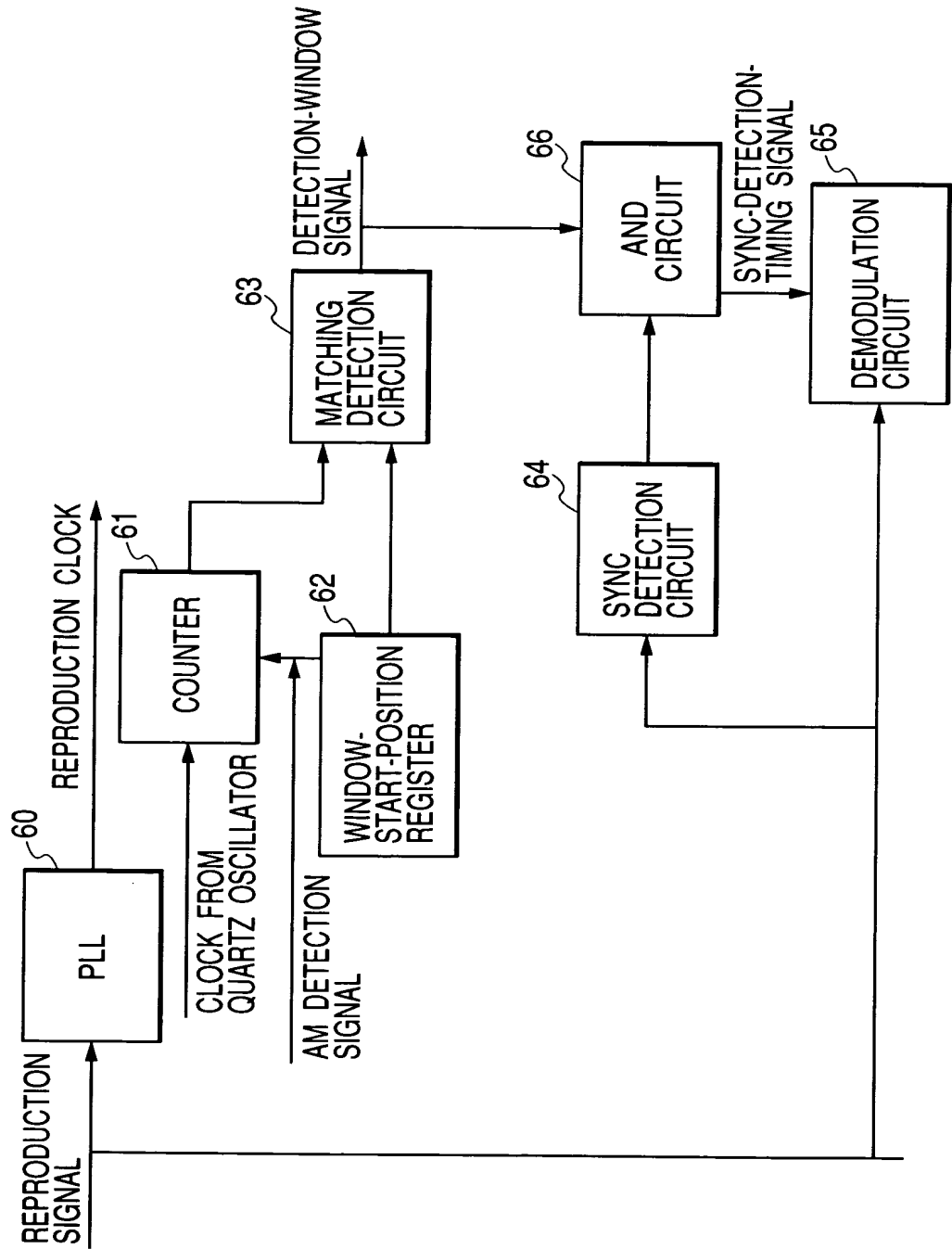


FIG. 14

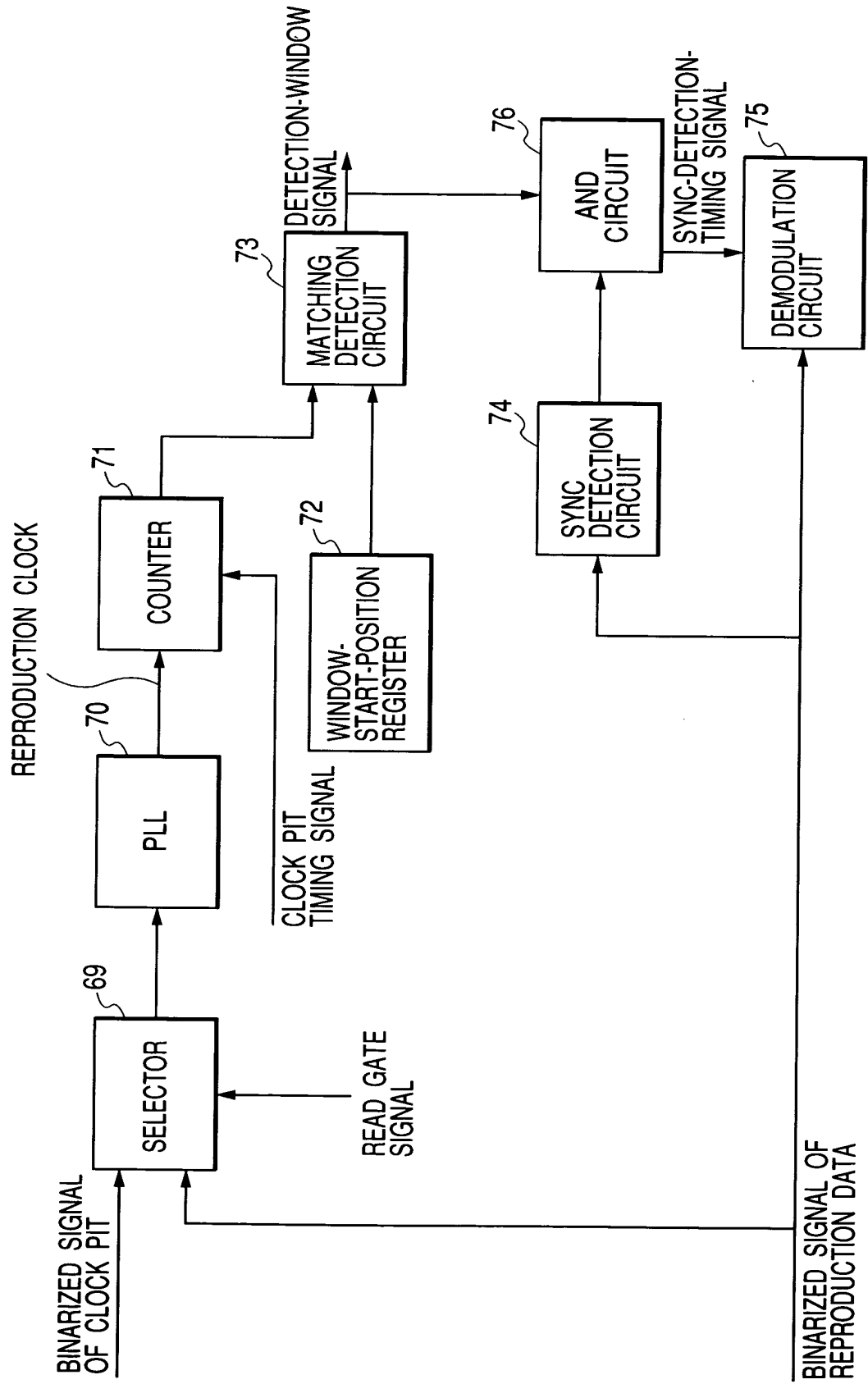


FIG. 15

